REMARKS/ARGUMENTS

Claims 2 and 5 were pending in this Application before Applicants' Amendment of April 1, 2008. In Applicants' Amendment of April 1, 2008, which is incorporated herein by reference, Applicants canceled claims 2 and 5. New claims 13-23 were added.

Applicants respectfully submit that support for the newly added claims can be found throughout the specification, the original claims, and the drawings. Claims 13-23 are now pending in the Application after entry of this Amendment. No new matter has been entered.

Applicants have added new claim 13 directed to the subject matter of cancelled claim 2. Thus, new claim 13 is similarly drawn to the elected Group 2 for a signal flow driven circuit physical synthesis technique, classified in class 716, subclass 18. For the purposes of illustration:

Canceled Claim 2	New Claim 13
A signal flow driven circuit physical synthesis	A computer-implemented method for physical
technique by	synthesis of integrated circuits, the method
	comprising:
	receiving information indicative of an
	integrated circuit;
tracing circuit signal flow so that,	tracing signal flow in the integrated circuit to
	determine a set of critical signal paths;
placing and routing circuit cell physical layout	placing and routing one or more circuit cells in
based on giving critical signal path with high	a physical layout associated with the integrated
priority are done by software program	circuit based on a priority associated with a
automatically.	critical signal path in the set of critical signal
	paths.

Applicants have also added new claim 15 directed to the subject matter of cancelled claim 5. Thus, new claim 15 is similarly drawn to the elected Group 2 for a signal

Appl. No. 10/762,781 Amdt. dated September 23, 2008 Reply to Office Action of September 4, 2008

flow driven circuit physical synthesis technique, classified in class 716, subclass 18. For the purposes of illustration:

Canceled Claim 5	New Claim 15
A method for performing circuit physical	A computer-implemented method for physical
synthesis utilizing:	synthesis of integrated circuits, the method
	comprising:
	receiving information indicative of an
	integrated circuit;
(a) generating a layout constraint using open	generating a plurality of circuit layout
circuit time constant technique on a multitude	constraints using an open circuit time constant
of critical nodes,	technique on each node in a plurality of critical
	nodes associated with the integrated circuit;
(b) applying the signal flow driven circuit	tracing signal flow within the integrated circuit
physical synthesis technique of claim 2, and	to determine a set of critical signal paths;
	placing and routing one or more circuit cells
	automatically in the physical layout associated
	with the integrated circuit based on a priority
	associated with a critical signal path in the set
	of critical signal paths.
(c) applying a signal flow driven circuit	partitioning the physical layout associated with
analysis technique tracing circuit signal flow	the integrated circuit based on functionality
so that, analyzing a circuit, and partitioning a	and criticality;
circuit is based on functionality and criticality,	
and generating a multitude of circuit layout	
constraints are done by a software program.	

Applicants have further added new claim 23 directed to the subject matter of cancelled claim 5. Thus, new claim 23 is similarly drawn to the elected Group 2 for a signal flow driven circuit physical synthesis technique, classified in class 716, subclass 18.

Appl. No. 10/762,781 Amdt. dated September 23, 2008 Reply to Office Action of September 4, 2008

Applicants respectfully submit the new claims 14 and 16-22 that depend either directly or indirectly from new claims 13 and 15, respectively are also readable on the elected Group 2. Applicants respectfully disagree with the Office Action that "only" claims drawn to non-elected inventions are presented. As discussed above, each of the new independent claims 13, 15, and 23, and their respective dependent claims, are directed to a signal flow driven circuit physical synthesis technique, classified in class 716, subclass 18 and this readable on the elected invention of Group 2.

CONCLUSION

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 925-472-5000.

Respectfully submitted,

/Sean F. Parmenter, Reg. No. 53,437/ Sean F. Parmenter Reg. No. 53,437

TOWNSEND and TOWNSEND and CREW LLP Two Embarcadero Center, Eighth Floor San Francisco, California 94111-3834 Tel: 925-472-5000

Tel: 925-472-5000 Fax: 415-576-0300

SFP:lls 61512575 v1